

BUR9-2001-0016-US1

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1c978 U.S. PTO  
10/060750  
01/30/02

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of

Devins et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: **SYSTEM FOR CONTROLLING EXTERNAL MODELS USED FOR VERIFICATION  
OF SYSTEM ON A CHIP (SOC) INTERFACES**

Assistant Commissioner of Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

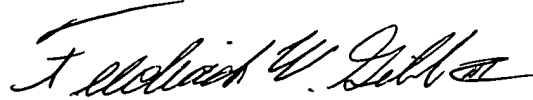
It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

BUR9-2001-0016-US1

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's

Deposit Account No. 09-0456.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Frederick W. Gibbs, III". The signature is fluid and cursive, with a prominent initial "F" and a long, sweeping underline.

Frederick W. Gibbs, III  
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